Recruitment Advt. No: IITDH/R&D/CSE/RK/Project-Recruitment/8.50/2020-21   **24 March 2021**

IIT Dharwad is an Institute of National Importance created by an Act of Parliament in 2016. IIT Dharwad has been steadily establishing its operations in its present campus while at the same time, master plan development and construction of permanent campus infrastructure are also in progress. To cater the Institute's temporary requirement under a project in the Department of Computer Science and Engineering; IIT Dharwad intends to hold a **Selection Process** for the following temporary position:

S. No.	Particulars	Details
1.	Name of the Position	Junior Research Fellow (On Contract)
2.	Number of Positions	1
3.	Essential Qualification	Post Graduate degree in Computer Science (or allied streams) or Graduate/Post Graduate degree in Computer Science (or allied streams) selected through a process described through any one of the following: a. Scholars who are selected through National Eligibility Test– CSIR-UGC NET including lectureship (Assistant Professorship) or GATE b. The Selection process through National level examination conducted by Central Govt. Departments and their agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc.
4.	Desirable Qualification	Strong programming skills Strong conceptual knowledge in Computer Architecture and Operating Systems
5.	Consolidated Emoluments	₹31,000/-+ 16% House Rent Allowance per month
6	Duration of the tenure	Maximum 2 years
7.	Tenure of appointment	The appointment for the above contractual position will be initially for 03 (three) months, which can be further extended subject to satisfactory performance, availability of funds in the project and requirement in the project.
8.	Age Limit	Not exceeding 32 years as on the date of selection process.

Job Description:

S. No.	Project Code and Title	Job Description
1.	CRG/2020/001001 Criticality-aware Hybrid Cache Hierarchy	Performing literature review Formulating research problems in the realm of Computer Organization and Processor Architecture Design, with a focus on memory hierarchies, NVRAM-based caches, and instruction criticality Implementing and evaluating past work as well as proposed ideas in a Java-based architectural simulator (Tejas: https://www.cse.iitd.ac.in/tejas/) Writing project report, research articles Delivering presentations, demos Administrative activities related to the project

Details of the Selection Process:

S. No.	Particulars	Details
1.	Date	Last date to submit application form online: 8th April 2021 Date for declaration of shortlist: 9th April 2021 Date of selection process: 10th April 2021
2.	Selection process	Link to application form: https://forms.gle/8yNY7iTRHtV5K8nf7 Applications will be initially screened, and shortlisted for a test, and further shortlisted for an interview. The test and interview will be conducted online. Mere fulfilment of minimum eligibility will not bestow the right for shortlisting. IIT Dharwad reserves the right to screen and shortlist candidates by introducing higher benchmarks. Emails will be sent to all shortlisted candidates.
3.	Schedule	Date and Time-slots will be communicated through email. Tentative date of written test and interviews: 10th April 2021. Any change in the schedule will be notified on the IIT Dharwad website.
4.	Documents Required	Applicant should have online version of all original certificates including mark sheets (matriculation onwards), experience certificate(s), monthly emoluments/salary drawn, and all other relevant documents (including one set of self-attested certificate(s)/document(s) and updated Curriculum Vitae.

General Instructions to the applicants:

1	Selected candidates shall not have claim on any regular position and shall not be bestowed any of the privileges like Housing, Medical Facility and Other benefits available to regular employees of the Institute.
2	Applicants are advised to ensure, before appearing for Selection Process, that they possess the minimum essential qualification and experience laid down for the post. Qualification obtained has to be from recognized University/Institute. Eligibility in terms of age, qualification and experience of a candidate shall be considered as on the date of Selection Process.
3	The number of vacancies indicated in the notification is tentative. IIT Dharwad reserves the right to increase or decrease the number of advertised posts at the time of selection. Further, IIT Dharwad reserves the right to NOT fill any of the posts advertised.
4	No TA/DA will be paid for attending the Selection Process.
5	In case of any inadvertent mistake in the process of selection, which may be detected at any stage even after issue of the appointment letter, the Institute reserves the right to *modify*, *withdraw* or *cancel* any communication made to the Applicants.
6	After joining the service of the Institute, the person(s) will have to abide by the Rules, Regulations, Ordinances, Statutes and Act of the Institute applicable from time to time. He/She may be assigned any duty within or outside the Institute depending upon the exigency of the work.
7	The Institute shall verify the antecedents or documents submitted by a person at the time of appointment or during the tenure of the service. In case it is detected that the documents submitted by the candidates are fake, or the person has a clandestine antecedents or background and has suppressed the said information, then his/her services shall be terminated forthwith and legal action may be initiated against such candidates/employees.
8	No interim correspondence whatsoever will be entertained from the candidates.
9	Canvassing in any form or bringing in any influence political or otherwise, will be a disqualification for the post.
10	In case of any dispute/ambiguity that may occur in the process of selection, the decision of the Institute shall be final.
11	For a query related to Selection Process, applicant may send email on rajshekar.k@iitdh.ac.in. However, enquiry/queries related to eligibility for the post/interpretation of rules will not be entertained.

Assistant Registrar (R&D)
Date: 24 March 2021